

| Ref # | Hits | Search Query   | DBs             | Default Operator | Plurals | Time Stamp       |
|-------|------|--|-----------------|------------------|---------|------------------|
| L1    | 0    | ((electrostatic near discharge) ESD) AND (positive near (potential\$1 voltage\$1) near tolerance) AND (negative near (potential\$1 voltage\$1) near tolerance) AND (device circuit IC (integrated adj circuit)) AND pad AND substrate AND well\$1 AND (float\$3 near state) AND (dop\$3 near (region\$1 area\$1)) AND (voltage near (path\$1 line\$1 system ground)) AND (isolat\$3 STI LOCOS FOX (field near oxide)).CLM. | US-PGPUB; USPAT | OR               | ON      | 2005/08/05 11:53 |